

REMARKS

In the Official Action mailed on **4 November 2005**, the Examiner reviewed claims 1-24. Claims 1-24 were rejected under 35 U.S.C. §102(e) as being anticipated by Foster (USPN 6,675,382, hereinafter “Foster”).

Rejections under 35 U.S.C. §102(e) and 35 U.S.C. §103(a)

Claims 1-24 were rejected as being anticipated by Foster. Applicant respectfully points out that at the time of invention, both Foster and the present invention were owned by or subject to assignment to Sun Microsystems, Inc., and that the rejection of claims 1-24 of the instant application should be an obviousness-type double patenting rejection.

Accordingly, Applicant respectfully submits the attached terminal disclaimer to obviate an obviousness-type double patenting rejection.

Hence, Applicant respectfully submits that independent claims 1, 9, and 17 as presently amended are in condition for allowance. Applicant also submits that claims 2-8, which depend upon claim 1, claims 10-16, which depend upon claim 9, and claims 18-24, which depend upon claim 17, are for the same reasons in condition for allowance and for reasons of the unique combinations recited in such claims.

CONCLUSION

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

By


Edward J. Grundler
Registration No. 47,615

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Edward J. Grundler
PARK, VAUGHAN & FLEMING LLP
2820 Fifth Street
Davis, CA 95616-7759
Tel: (530) 759-1663
FAX: (530) 759-1665
Email: edward@parklegal.com